

18 Bridle Path Lane  
Bluffton, SC 29910

May 8, 2015

Dr. Ramesh Subramanian  
Communications Officer SIGCIS, Society for the History of Technology  
Quinnipiac University School of Business and Engineering  
Faculty Office Building 126  
275 Mount Carmel Avenue  
Hamden, CT 06518

Dear Dr. Ramesh Subramanian:

I respectfully request your assistance as Communications Officer of SIGCIS and member of the Society for the History of Technology in reviewing the enclosed information.

I am the inventor of the parallel processor and digital spread sheet. In 1973 I started the patent process for the parallel processor operating system and digital spreadsheet, both theory and circuitry.

The computer described in this patent, United States Patent 4,391,447, is a demonstration machine showing how chess pieces (icons) move and interact with each other on the chessboard (spreadsheet) in a visual display.

The chess computer/parallel processor is not a program but a complete operating system using logic blocks and a digital spreadsheet.

The development of the parallel processor/spreadsheet/non Von Neuman architecture is the story of the development of the chess computer.

Please refer to [www.chessilluminated.com](http://www.chessilluminated.com) or [www.parallelprocessor.info](http://www.parallelprocessor.info).  
The website, like the chess computer, is simplicity hiding complexity.

The Society for the History of Technology is “dedicated to the historical study of technology and its relations with politics, economics, labor, business, the environment, public policy, science, and the arts.”

The scientific establishment has not recognized or attributed this important breakthrough accurately. I am asking for your help in correcting the historical record.

Sincerely,



Raymond L. Dudley